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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

The Application of: **Mitsuaki HORI et al.**

Group Art Unit: **2826**

Serial No.: **10/662,384**

Examiner: **Ahmed N. Sefer**

Filed: **September 16, 2003**

Confirmation Number: **2168**

For: **SEMICONDUCTOR DEVICE, MANUFACTURE AND  
EVALUATION METHODS FOR SEMICONDUCTOR DEVICE,  
AND PROCESS CONDITION EVALUATION METHOD**

*Submission*

Attorney Docket Number: **031111**

Customer Number: **38834**

*w/PCB*

*9/28/05*

**AMENDMENT UNDER 37 C.F.R. §1.116**

Mail Stop: AF

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

September 6, 2005

Sir:

This paper is filed in response to the Office Action dated June 6, 2005.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of  
this paper.

Remarks/Arguments begin on page 9 of this paper.